Assembly Language for Intel-Based Computers, $5^{\text {th }}$ Edition

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## Chapter 1: Basic Concepts

## Chapter Overview

- Welcome to Assembly Language
- Virtual Machine Concept
- Data Representation
- Boolean Operations


## Welcome to Assembly Language

- Some Good Questions to Ask
- Assembly Language Applications


## Questions to Ask

- Why am I learning Assembly Language?
- What background should I have?
- What is an assembler?
- Convert source code programs from assembly language into machine language
- What hardware/software do I need?
- Intel386...
- MASM
- Editor
- What types of programs will I create?
- 16-bit real-address mode
- 32-bit protected mode
- What do I get with this book?
- What will I learn?


## Welcome to Assembly Language (cont)

- How does assembly language ( AL ) relate to machine language?
- Machine language is a numeric language specifically understood by a computer processor
- Assembly language consists of statements written with short menmonics
- How do C++ and Java relate to AL?
- Have one-to-many relationship with assembly language and machine language
- Is AL portable?
- Not portable because it is designed for a specific processor family
- Why learn AL?
- Write
- embedded programs
- Real-time applications
- .....


## Assembly Language Applications

- Some representative types of applications:
- Business application for single platform
- Hardware device driver
- Business application for multiple platforms
- Embedded systems \& computer games
(see next panel)


## Comparing ASM to High-Level Languages

| Type of Application | High-Level Languages | Assembly Language |
| :--- | :--- | :--- |
| Business application soft- <br> ware, written for single <br> platform, medium to large <br> size. | Formal structures make it easy to <br> organize and maintain large sec- <br> tions of code. | Minimal formal structure, so one <br> must be imposed by program- <br> mers who have varying levels of <br> experience. This leads to difficul- <br> ties maintaining existing code. |
| Hardware device driver. | Language may not provide for <br> direct hardware access. Even if it <br> does, awkward coding techniques <br> must often be used, resulting in <br> maintenance difficulties. | Hardware access is straightfor- <br> ward and simple. Easy to main- <br> tain when programs are short and <br> well documented. |
| Business application written <br> for multiple platforms (dif- <br> ferent operating systems). | Usually very portable. The source <br> code can be recompiled on each <br> target operating system with mini- <br> mal changes. | Must be recoded separately for <br> each platform, often using an <br> assembler with a different syn- <br> tax. Difficult to maintain. |
| Embedded systems and <br> computer games requiring <br> direct hardware access. | Produces too much executable <br> code, and may not run efficiently. | Ideal, because the executable <br> code is small and runs quickly. |

## What's Next

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## Virtual Machine Concept

- Virtual Machines
- Specific Machine Levels


## Virtual Machines

- Tanenbaum: Virtual machine concept
- Programming Language analogy:
- Each computer has a native machine language (language LO) that runs directly on its hardware
- A more human-friendly language is usually constructed above machine language, called Language L1
- Programs written in L1 can run two different ways:
- Interpretation - L0 program interprets and executes L1 instructions one by one
- Translation - L1 program is completely translated into an L0 program, which then runs on the computer hardware


## Translating Languages

English: Display the sum of A times B plus C.


## Specific Machine Levels


(descriptions of individual levels follow . . .)

## High-Level Language

- Level 5
- Application-oriented languages
- C++, Java, Pascal, Visual Basic . . .
- Programs compile into assembly language (Level 4)


## Assembly Language

- Level 4
- Instruction mnemonics that have a one-toone correspondence to machine language
- Calls functions written at the operating system level (Level 3)
- Programs are translated into machine language (Level 2)


## Operating System

- Level 3
- Provides services to Level 4 programs
- Translated and run at the instruction set architecture level (Level 2)


## Instruction Set Architecture

- Level 2
- Also known as conventional machine language
- Executed by Level 1 (microarchitecture) program


## Microarchitecture

- Level 1
- Interprets conventional machine instructions (Level 2)
- Executed by digital hardware (Level 0)


## Digital Logic

- Level 0
- CPU, constructed from digital logic gates
- System bus
- Memory
- Implemented using bipolar transistors
next: Data Representation


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## Data Representation

- Binary Numbers
- Translating between binary and decimal
- Binary Addition
- Integer Storage Sizes
- Hexadecimal Integers
- Translating between decimal and hexadecimal
- Hexadecimal subtraction
- Signed Integers
- Binary subtraction
- Character Storage


## Binary Numbers

- Digits are 1 and 0
- 1 = true
- 0 = false
- MSB - most significant bit
- LSB - least significant bit
- Bit numbering: $\begin{array}{r}\text { LSB } \\ \hline 15011001010011100 \\ \hline 15\end{array}$


## Binary Numbers

- Each digit (bit) is either 1 or 0
- Each bit represents a power of 2 :

```
|\\l|
    2}\begin{array}{llllllll}{\mp@subsup{7}{}{7}}&{\mp@subsup{2}{}{6}}&{\mp@subsup{2}{}{5}}&{\mp@subsup{2}{}{4}}&{\mp@subsup{2}{}{3}}&{\mp@subsup{2}{}{2}}&{\mp@subsup{2}{}{1}}&{\mp@subsup{2}{}{0}}
```

Table 1-3 Binary Bit Position Values.

Every binary number is a sum of powers of 2

| $\mathbf{2}^{\boldsymbol{n}}$ | Decimal Value | $\mathbf{2}^{\boldsymbol{n}}$ | Decimal Value |
| :---: | :---: | :---: | :---: |
| $2^{0}$ | 1 | $2^{8}$ | 256 |
| $2^{1}$ | 2 | $2^{9}$ | 512 |
| $2^{2}$ | 4 | $2^{10}$ | 1024 |
| $2^{3}$ | 8 | $2^{11}$ | 2048 |
| $2^{4}$ | 16 | $2^{12}$ | 4096 |
| $2^{5}$ | 32 | $2^{13}$ | 8192 |
| $2^{6}$ | 64 | $2^{14}$ | 16384 |
| $2^{7}$ | 128 | $2^{15}$ | 32768 |

## Translating Binary to Decimal

Weighted positional notation shows how to calculate the decimal value of each binary bit:
$d e c=\left(D_{n-1} \times 2^{n-1}\right)+\left(D_{n-2} \times 2^{n-2}\right)+\ldots+\left(D_{1} \times 2^{1}\right)+\left(D_{0} \times 2^{0}\right)$
$\mathrm{D}=$ binary digit
binary $00001001=$ decimal 9 :

$$
\left(1 \times 2^{3}\right)+\left(1 \times 2^{0}\right)=9
$$

## Translating Unsigned Decimal to Binary

- Repeatedly divide the decimal integer by 2. Each remainder is a binary digit in the translated value:

| Divilsion | Quotient | Remainder |
| :---: | :---: | :---: |
| 37 | $/ 2$ | 18 |
| 18 | $/ 2$ | 9 |
| 9 | 2 | 4 |
| 4 | 2 | 2 |
| $2 / 2$ | 1 | 0 |
| $1 / 2$ | 0 | 0 |

$$
37=100101
$$

## Binary Addition

- Starting with the LSB, add each pair of digits, include the carry if present.

| carry: 1 |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
|  | (4) |  |  |  |  |  |  |
| 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 |

## Integer Storage Sizes



Table 1-4 Ranges of Unsigned Integers.

| Storage Type | Range (low-high) | Powers of 2 |
| :--- | :--- | :--- |
| Unsigned byte | 0 to 255 | 0 to $\left(2^{8}-1\right)$ |
| Unsigned word | 0 to 65,535 | 0 to $\left(2^{16}-1\right)$ |
| Unsigned doubleword | 0 to $4,294,967,295$ | 0 to $\left(2^{32}-1\right)$ |
| Unsigned quadword | 0 to $18,446,744,073,709,551,615$ | 0 to $\left(2^{64}-1\right)$ |

What is the largest unsigned integer that may be stored in 20 bits?

## Hexadecimal Integers

Binary values are represented in hexadecimal.

Table 1-5 Binary, Decimal, and Hexadecimal Equivalents.

| Binary | Decimal | Hexadecimal | Binary | Decimal | Hexadecimal |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0000 | 0 | 0 | 1000 | 8 | 8 |
| 0001 | 1 | 1 | 1001 | 9 | 9 |
| 0010 | 2 | 2 | 1010 | 10 | $A$ |
| 0011 | 3 | 3 | 1011 | 11 | B |
| 0100 | 4 | 4 | 1100 | 12 | $C$ |
| 0101 | 5 | 5 | 1101 | 13 | $D$ |
| 0110 | 6 | 7 | 1110 | 14 | $E$ |
| 0111 | 7 |  | 1111 | 15 | $F$ |

## Translating Binary to Hexadecimal

- Each hexadecimal digit corresponds to 4 binary bits.
- Example: Translate the binary integer 000101101010011110010100 to hexadecimal:

| 1 | 6 | A | 7 | 9 | 4 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0001 | 0110 | 1010 | 0111 | 1001 | 0100 |

## Converting Hexadecimal to Decimal

- Multiply each digit by its corresponding power of 16:

$$
\operatorname{dec}=\left(D_{3} \times 16^{3}\right)+\left(D_{2} \times 16^{2}\right)+\left(D_{1} \times 16^{1}\right)+\left(D_{0} \times 16^{0}\right)
$$

- Hex 1234 equals $\left(1 \times 16^{3}\right)+\left(2 \times 16^{2}\right)+\left(3 \times 16^{1}\right)+\left(4 \times 16^{0}\right)$, or decimal 4,660.
- Hex 3BA4 equals $\left(3 \times 16^{3}\right)+\left(11 * 16^{2}\right)+\left(10 \times 16^{1}\right)+\left(4 \times 16^{0}\right)$, or decimal 15,268.


## Powers of 16

Used when calculating hexadecimal values up to 8 digits long:

| $16^{\mathbf{n}}$ | Decimal Value | $\mathbf{1 6}^{\boldsymbol{n}}$ | Decimal Value |
| :--- | :--- | :--- | :--- |
| $16^{0}$ | 1 | $16^{4}$ | 65,536 |
| $16^{1}$ | 16 | $16^{5}$ | $1,048,576$ |
| $16^{2}$ | 256 | $16^{6}$ | $16,777,216$ |
| $16^{3}$ | 4096 | $16^{7}$ | $268,435,456$ |

## Converting Decimal to Hexadecimal

| Division | Quotient | Remainder |
| :---: | :---: | :---: |
| $422 / 16$ | 26 | 6 |
| $26 / 16$ | 1 | A |
| $1 / 16$ | 0 | 1 |

decimal $422=1 \mathrm{~A} 6$ hexadecimal

## Hexadecimal Addition

- Divide the sum of two digits by the number base (16). The quotient becomes the carry value, and the remainder is the sum digit.


Important skill: Programmers frequently add and subtract the addresses of variables and instructions.

## Hexadecimal Subtraction

- When a borrow is required from the digit to the left, add 16 (decimal) to the current digit's value:


Practice: The address of var 1 is 00400020 . The address of the next variable after var1 is 0040006A. How many bytes are used by var1?

## Signed Integers

The highest bit indicates the sign. $1=$ negative, $0=$ positive


If the highest digit of a hexadecimal integer is $>7$, the value is negative. Examples: 8A, C5, A2, 9D

## Forming the Two's Complement

- Negative numbers are stored in two's complement notation
- Represents the additive Inverse

| Starting value | 00000001 |
| :--- | ---: |
| Step 1: reverse the bits | 11111110 |
| Step 2: add 1 to the value from Step 1 | 11111110 <br> +00000001 |
| Sum: two's complement representation | 11111111 |

Note that $00000001+11111111=00000000$

## Binary Subtraction

- When subtracting $A-B$, convert $B$ to its two's complement
- Add A to (-B)


$$
\text { Practice: Subtract } 0101 \text { from } 1001 .
$$

## Learn How To Do the Following:

- Form the two's complement of a hexadecimal integer
- Convert signed binary to decimal
- Convert signed decimal to binary
- Convert signed decimal to hexadecimal
- Convert signed hexadecimal to decimal


## Ranges of Signed Integers

The highest bit is reserved for the sign. This limits the range:

| Storage Type | Range (low-high) | Powers of 2 |
| :--- | :--- | :--- |
| Signed byte | -128 to +127 | $-2^{7}$ to $\left(2^{7}-1\right)$ |
| Signed word | $-32,768$ to $+32,767$ | $-2^{15}$ to $\left(2^{15}-1\right)$ |
| Signed doubleword | $-2,147,483,648$ to $2,147,483,647$ | $-2^{31}$ to $\left(2^{31}-1\right)$ |
| Signed quadword | $-9,223,372,036,854,775,808$ to <br> $+9,223,372,036,854,775,807$ | $-2^{63}$ to $\left(2^{63}-1\right)$ |

Practice: What is the largest positive value that may be stored in 20 bits?

## Character Storage

- Character sets
- Standard ASCII (0 - 127)
- Extended ASCII (0 - 255)
- ANSI (0 - 255)
- Unicode (0-65,535)
- Null-terminated String
- Array of characters followed by a null byte
- Using the ASCII table
- back inside cover of book


## Numeric Data Representation

- pure binary
- can be calculated directly
- ASCII binary
- string of digits: "01010101"
- ASCII decimal
- string of digits: "65"
- ASCII hexadecimal
- string of digits: "9C"


## What's Next

- Welcome to Assembly Language
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- Data Representation
- Boolean Operations


## Boolean Operations

- NOT
- AND
- OR
- Operator Precedence
- Truth Tables


## Boolean Algebra

- Based on symbolic logic, designed by George Boole
- Boolean expressions created from:
- NOT, AND, OR

| Expression | Description |
| :--- | :--- |
| $\neg \mathrm{X}$ | NOT X |
| $\mathrm{X} \wedge \mathrm{Y}$ | X AND Y |
| $\mathrm{X} \vee \mathrm{Y}$ | X OR Y |
| $\neg \mathrm{X} \vee \mathrm{Y}$ | $($ NOT X$)$ OR Y |
| $\neg(\mathrm{X} \wedge \mathrm{Y})$ | NOT $(\mathrm{X}$ AND Y$)$ |
| $\mathrm{X} \wedge \neg \mathrm{Y}$ | X AND $($ NOT Y$)$ |

## NOT

- Inverts (reverses) a boolean value
- Truth table for Boolean NOT operator:

| $\mathbf{X}$ | $\neg \mathbf{X}$ |
| :---: | :---: |
| F | T |
| T | F |

Digital gate diagram for NOT:


## AND

- Truth table for Boolean AND operator:

| $\mathbf{X}$ | $\mathbf{Y}$ | $\mathbf{X} \wedge \mathbf{Y}$ |
| :---: | :---: | :---: |
| $F$ | $F$ | $F$ |
| $F$ | $T$ | $F$ |
| $T$ | $F$ | $F$ |
| $T$ | $T$ | $T$ |

Digital gate diagram for AND:


## OR

- Truth table for Boolean OR operator:

| $\mathbf{X}$ | $\mathbf{Y}$ | $\mathbf{X} \vee \mathbf{Y}$ |
| :---: | :---: | :---: |
| F | F | F |
| F | T | T |
| T | F | T |
| T | T | T |

Digital gate diagram for OR:


## Operator Precedence

- Examples showing the order of operations:

| Expression | Order of Operations |
| :--- | :--- |
| $\neg \mathrm{X} \vee \mathrm{Y}$ | NOT, then OR |
| $\neg(\mathrm{X} \vee \mathrm{Y})$ | OR, then NOT |
| $\mathrm{X} \vee(\mathrm{Y} \wedge \mathrm{Z})$ | AND, then OR |

## Truth Tables (1 of 3)

- A Boolean function has one or more Boolean inputs, and returns a single Boolean output.
- A truth table shows all the inputs and outputs of a Boolean function

Example: $\neg X \vee Y$

| $\mathbf{X}$ | $\neg \mathbf{X}$ | $\mathbf{Y}$ | $\neg \mathbf{X} \vee \mathbf{Y}$ |
| :---: | :---: | :---: | :---: |
| $F$ | $T$ | $F$ | $T$ |
| $F$ | $T$ | $T$ | $T$ |
| $T$ | $F$ | $F$ | $F$ |
| $T$ | $F$ | $T$ | $T$ |

## Truth Tables (2 of 3)

- Example: $\mathrm{X} \wedge \neg \mathrm{Y}$

| X | Y | $\neg \mathbf{Y}$ | $\mathbf{X} \wedge \neg \mathbf{Y}$ |
| :---: | :---: | :---: | :---: |
| F | F | T | F |
| F | T | F | F |
| T | F | T | T |
| T | T | F | F |

## Truth Tables (3 of 3)

- Example: $(\mathrm{Y} \wedge \mathrm{S}) \vee(\mathrm{X} \wedge \neg \mathrm{S})$

| X | Y | S | $\mathrm{Y} \wedge \mathbf{S}$ | $\neg \mathbf{S}$ | $\mathbf{X} \wedge \neg \mathbf{S}$ | $(\mathbf{Y} \wedge \mathbf{S}) \vee(\mathbf{X} \wedge \neg \mathbf{S})$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| F | F | F | F | T | F | F |
| F | T | F | F | T | F | F |
| T | F | F | F | T | T | T |
| T | T | F | F | T | T | T |
| F | F | T | F | F | F | F |
| F | T | T | T | F | F | T |
| T | F | T | F | F | F | F |
| T | T | T | T | F | F | T |



Two-input multiplexer

## Summary

- Assembly language helps you learn how software is constructed at the lowest levels
- Assembly language has a one-to-one relationship with machine language
- Each layer in a computer's architecture is an abstraction of a machine
- layers can be hardware or software
- Boolean expressions are essential to the design of computer hardware and software


## 5468652045 6E 64

## What do these numbers represent?

