1. Given the logic circuit
   (a) Obtain its Boolean function. (10%)
   (b) Obtain its truth table. (20%)
   (c) Express it in sum of minterms and product of maxterms with literals. (20%)

2. Given the Boolean function \( F(A, B, C) = (A' + B)(B' + C) \)
   (a) Implement the function with gates. (10%)
   (b) Obtain the truth table of the function. (20%)
   (c) Express it in sum of minterms and product of maxterms with symbols \( \Sigma \) and \( \Pi \). (20%)